Chi Zhang

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Research Interests

- Deep learning accelerators and applications

Appointments

- Research Assistant advised by professor Viktor Prasanna

Education

University of Southern California 2017 - present
- Ph.D. student in Computer Science

University of Southern California 2015 - 2016
- M.S. in Electrical Engineering
- Cumulative GPA: 4.0/4.0

Southeast University, Nanjing, China 2011 - 2015
- B.S. in Chien-Shiung Wu Honors College
- Major in Information Science and Engineering
- Overall GPA: 88/100, Major GPA: 89/100

Research Experience

FPGA and Parallel Computing Groups, USC 2015 - 2016
- Develop a high throughput sorting architecture on Intel QuickAssist QPI FPGA Platform
- Develop a highly optimized Convolutional Neural Network accelerator on Intel QuickAssist QPI FPGA Platform

Academic Honors

MS Honors Program
USC Ming Hsieh Department of Electrical Engineering Fall 2016

Computer Skills

Programming Languages: C/C++ (Pthread, OpenMP), Java, Scala Python + Numpy, Verilog, SystemVerilog, MATLAB, LaTeX
Deep Learning Frameworks: Keras, TensorFlow
Software: VCS Simulator, Modelsim, DC Compiler, ICC Compiler, Quartus, ISE, Simulink, Xcode, Intellij, PyCharm, Jupyter Notebook
GitHub Account: https://github.com/vermouth1992

Academic Experience

Royal Institute of Technology (KTH), Stockholm, Sweden Spring 2015
- Last semester exchange program for bachelor thesis
- Model and verify an All-digital Phase-locked Loop design using MATLAB and Simulink

Publications

Chi Zhang, Viktor Prasanna, Frequency Domain Acceleration of Convolutional Neural Networks on CPU-FPGA Shared Memory System, ACM/SIGDA International Symposium on Field Programmable Gate Arrays (FPGA), 2017.

Chi Zhang, Ren Chen, Viktor Prasanna, High Throughput Large-Scale Sorting on a CPU-FPGA Heterogeneous Platform, 30th Annual International Parallel & Distributed Processing Symposium Workshop (IPDPSW), 2016.

Teaching Experience

University of Southern California

- Teaching Assistant: CSCI 350 Introduction to Operating Systems, Fall 2017