

Vasanth Krishna Namasivayam

(213) 741-1186

namasiva@usc.edu

1039 W 23 St, Apt # 204, Los Angeles – 90007

OBJECTIVE To obtain an internship that could develop into a full time position in Electrical Engineering

EDUCATION M.S. Electrical Engineering, Viterbi School of Engineering Expected Graduation Dec 2005
University of Southern California, Los Angeles, CA GPA: 3.6 / 4.0

B.E. Electronics & Communications Engineering June 2003
Sir M Visvesvaraya Institute of Technology, Bangalore, India First Class with Distinction

PROJECTS Graduate

- Performance profiling of Xilinx Microblaze (in progress)
- Analysis of embedded systems that use reconfigurable computing (in progress)
- Presented seminars on the ISA and memory architectures of embedded processors Altera Nios 2 and Sun Niagara
- Implemented random restart hill climbing algorithm for n queen chess problem in C++
- Implemented a Bayesian network in Genie and used queries to deduce probability distributions
- Implemented the tic-tac-toe game in Electronic Product Designer
- Prepared an ontology for semantic web and made intelligent inferences
- Designed and simulated a 32 bit pipelined MIPS processor in ePD
- Designed a traditional memory interface for 80486 microprocessor

Undergraduate

- Designed and simulated an eight channel UART in Active HDL
- Center for Department for Telecommunications, India
- Implemented a low cost audio video modulator and transmitter
- Implemented a bus terminal display and tracking system

WORK EXPERIENCE

- Grader and Special Course Assistant – System Design Using Microprocessors Spring 2005
Prof Gandhi Puvvada, University of Southern California
- Teaching Assistant – Introduction to Digital Circuits Fall 2004
Prof Gandhi Puvvada, University of Southern California
- Student Intern – Design of Eight Channel UART Spring 2003
Center for Department for Telecommunications, India

SKILLS

Software – C, C ++, VHDL, Assembly Language (MIPS, 8086, 8051)
Tools – Active HDL, Electronic Product Designer, Genie
Operating Systems – Win 9x/NT/XP, Linux, UNIX
Web Technologies – HTML, Macromedia Flash

RELEVANT COURSES

Computer Organization and Design, Advanced Computer Architecture, System Design using Microprocessors, Artificial Intelligence, VLSI Architectures, Real Time Systems, Logic Design and Switching Theory

HONORS

- Selected as Teaching Assistant under Prof. Gandhi Puvvada
- Selected as Special Course Assistant under Prof. Gandhi Puvvada
- Awarded gold medal for securing distinctions in all semesters in B.E
- GRE – 1530/1600 (99 percentile – verbal, 96 percentile – quantitative)
- University of New South Wales (International Competition for Schools)
 - Distinction in Science – 94th percentile in India
 - Distinction in English – 96th percentile in India

AFFILIATIONS IEEE Student member