

Name: \_\_\_\_\_

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**CS 410 Final Exam**  
**Fall 2003 [Bono]**  
December 10, 2003

There are 13 problems on the exam, with 92 points total available. There are 10 pages to the exam, including this one; make sure you have all of them. If you need additional space to write any answers, you may use the backs of pages (just direct us to look there).

Put your name and USC loginid at the top of the exam. Please read over the whole test before beginning. Good luck!

	<b>value</b>	<b>score</b>
Probs 1 - 3	10 pts.	
Problem 4 - 5	6 pts.	
Problem 6	5 pts.	
Problem 7	4 pts.	
Problem 8	15 pts.	
Problem 9 - 10	9 pts.	
Problem 11	9 pts.	
Problem 12	15 pts.	
Problem 13	19 pts.	
<b>TOTAL</b>	92 pts.	

### Problem 1 [4 pts.]

**Part A [2].** A symbol table for one scope is a collection of key-value pairs. During semantic analysis, what is the value we associate with a key?

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**Part B [2].** Symbol tables built during semantic analysis are still useful in the code generation phase. Name a value a compiler might add to existing symbol table entries during code generation (i.e., an additional value to associate with the key).

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### Problem 2 [4 pts.]

Short answer problems about semantic analysis. For the following questions assume have an AST built during the parsing phase, and that we have already built the inheritance hierarchy.

**Part A [1].** Processing the *attribute and method declarations* in Cool can be done in one pass over the classes by processing the *classes* in what order?

- basic classes followed by user-defined classes
- preorder traversal of inheritance hierarchy
- postorder traversal of inheritance hierarchy
- any order works

**Part B [1].** Processing the *expressions* in Cool can be done in one pass over the classes by processing the *classes* in what order?

- basic classes followed by user-defined classes
- preorder traversal of inheritance hierarchy
- postorder traversal of inheritance hierarchy
- any order works

**Part C [2].** The two tasks mentioned in parts A and B must each be done as a separate pass. What scoping rule(s) of Cool necessitates the two passes? (Note: this is *not* a rule in a lot of other languages, including C++.)

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### Problem 3 [2 pts.]

The parser *bison* constructs is most similar to which kind of parser listed below:

- SLR(1)
- LL(1)
- DFA
- recursive descent

### Problem 4 [2 pts.]

What formal language is use to describe patterns for tokens for lexical analysis:

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### Problem 5 [4 pts.]

Cool objects have information about what class they are stored right in the object at runtime. Name two features of Cool that requires type-specific information for an object at run-time:

1. \_\_\_\_\_
2. \_\_\_\_\_

### Problem 6 [5 pts.]

In class we discussed that a useful application of *dead-code elimination* would be for code that is only used in the debugging phase of program development. In PA4 much of our output was controlled by a debug flag `semant_debug`. E.g., `semant.cc` has code such as,

```
if (semant_debug) {  
    <code to print our inheritance hierarchy>  
}
```

This flag gets turned on when we run with the `-s` flag. E.g.,

```
mysemant -s testprog.cl
```

Can the C++ compiler eliminate the debug code from our `semant.cc`? **YES / NO**  
Why or why not?

### Problem 7 [4 pts.]

Would it be possible to apply the *loop version of reduction-in-strength* optimization to the following loop? **YES / NO** Why or why not?,

```
for (int i = 0; i < n; i+=2) {  
    A[i] = readData(cin);  
}
```

### Problem 8 [15 pts.]

Consider the following grammar over the alphabet  $\{(, )\}$ :

$$\begin{aligned} S &\rightarrow S S \\ &| ( S ) \\ &| ( ) \end{aligned}$$

**Part A.** Do any grammar transformations necessary to make the grammar suitable for LL(1) parsing. Circle and label each of the resulting grammar(s) with the transformation(s) you applied (or say “same” for that part if no transformations were necessary).

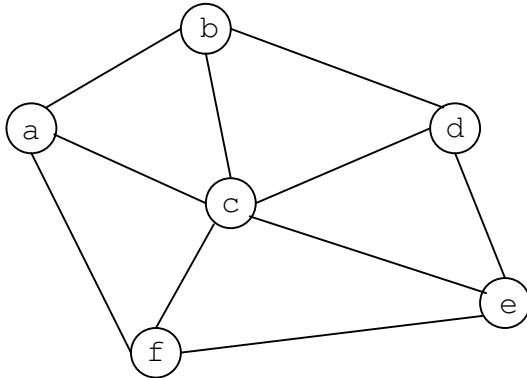
**Part B.** Attempt to create the LL(1) parse table for the final grammar you gave in part A. Show the complete table: if this grammar is not LL(1), that is if there are conflicts, show all the values that could go in a table entry.

Show your work.

Circle and label your answers to each of the parts.

### Problem 9 [4 pts.]

Consider the following *register interference graph* for variables a-f.



**Part A.** Name two variables whose values would have to be stored in *different* registers.

**Part B.** Name two variables whose values could be stored in the *same* register, or write *none* if there are none.

### Problem 10 [5 pts.]

Give a formula for computing the byte offset for the array reference  $\mathbf{a}[\mathbf{i}][\mathbf{j}][\mathbf{k}]$ , given the C array declaration shown below. Assume that arrays are stored in row-major order, and that `ints` are 4 bytes long. Hint: in row-major order, the rightmost subscript changes fastest when looking at where n-dim indices are stored in memory (e.g., the first three words of the array contain `a[0][0][0]`, `a[0][0][1]`, `a[0][0][2]`, etc.).

```
int a[DIM1][NROWS][NCOLS];
```

### **Problem 11 [9 pts.]**

Show the values printed by the following program assuming various parameter-passing schemes given below:

```
int A[2];

void swap (int x, int y)
{
    int temp = x;
    x = y;
    y = temp;
    A[1] = 2;
}

main ()
{
    int i, j;
    i = 1;
    A[0] = 4;
    A[1] = 3;
    swap (i, A[i]);
    cout << i << " " << A[0] << " " << A[1] << endl;
}
```

**Part A.** Call by value

**Part B.** Call by reference

**Part C.** Call-by-value-return

### Problem 12 [15 pts.]

Consider the following basic block. Assume all of the programmer variables, a through d, are live at the end of the basic block, and the temporary variables, t1 through t8, are not live at the end of the basic block. *Label and circle your final answers for each part.*

**Part A.** Show the DAG representation for this basic block.

**Part B.** Generate an improved 3AC sequence from your DAG.

```
t1 = c * d;  
t2 = b + t1;  
t3 = c * d;  
t4 = b + t3;  
t5 = c * d;  
t6 = b - t5;  
t7 = t4 / t6;  
t8 = t2 + t7;  
a = t8;
```

### Problem 13 [19 pts.]

Consider a new C statement, **if3**. **if3** is a three-way if that goes to a different branch depending on the whether a comparison comes out as **<**, **==**, or **>**. Here is its syntax:

```
stmt → if3 (expr1 ? expr2) {  
    < : stmt ;  
    == : stmt ;  
    > : stmt ;  
}
```

This new statement would be useful for implementing binary search. For example:

```
while (!emptyRange(low, high) && (!found)) {  
    mid = (low + high) / 2;  
    if3 (targetValue ? arr[mid]) {  
        < : high = mid - 1;  
        == : found = true;  
        > : low = mid + 1;  
    }  
}
```

**Part A [4].** If we added this construct to a C compiler, would the lexical analyzer need to be changed? **YES / NO**

If so how? (i.e., describe the rules that would be needed to be added to a flex file). If not, why not?

**Part B [15].** Write a C++ member function for generating 3-address code for this new construct from an AST representation of the code. Here is the AST structure for this construct:

```
class if3 : Statement {  
protected:  
    Expression *expr1;  
    Expression *expr2;  
    Statement *LTstmt; // less than (LT) branch  
    Statement *EQstmt; // equal to (EQ) branch  
    Statement *GTstmt; // greater than (GT) branch  
public:  
    virtual void genCode(); // you write this function  
    . . .  
};
```

(Don't put your answer here. Problem continued next page.)



### Problem 13 (cont.)

Tree structure reprinted for your convenience:

```
class if3 : Statement {
protected:
    Expression *expr1;
    Expression *expr2;
    Statement *LTstmt; // less than (LT) branch
    Statement *EQstmt; // equal to (EQ) branch
    Statement *GTstmt; // greater than (GT) branch
public:
    virtual void genCode(); // you write this function
    . . .
};
```

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```
void if3::genCode()
{
```